R5

III B.Tech II Semester (R05) Supplementary Examinations, April/May 2011 SOFTWARE TESTING METHODOLOGIES (Computer Science & Engineering, Information Technology)

Computer Science & Engineering, Information Technology)

Max Marks: 80

Time: 3 hours

Code: R5320506

Answer any FIVE questions All questions carry equal marks

- 1. (a) Distinguish between designer and the tester.
 - (b) Discuss the influence of effective modularity on testing.
- 2. (a) State and explain various kinds of predicate blindness with suitable examples.
 - (b) What are link counters? Discuss their use in path testing.
- 3. (a) State and explain various transaction flow junctions and mergers.
 - (b) Explain the terms Inspections, Reviews and Walkthroughs.
- 4. (a) Discuss about interface Range/Domain compatibility testing.
 - (b) Discuss about linearizing transformations in domain testing.
- 5. (a) Define structured flow graph. Explain lower path count Arithmetic
 - (b) What is the looping probability of a path expression? Write arithmetic rules. Explain with an example.
- 6. (a) How can we determine paths and domains in the logic-based testing?
 - (b) How the Boolean expressions can be used in the test case design?
- 7. What is meant by state testing? Discuss the testability tips?
- 8. (a) Write Partitioning Algorithm.
 - (b) Write an algorithm for All Pairs Paths using Matrix Operations.
